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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/560,203	12/08/2005	Stephen James Todd	GB920030063US1 3212		
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Kunzler & McKenzie 8 EAST BROADWAY SUITE 600 SALT LAKE CITY, UT 84111			BROPHY, MATTHEW J		
			ART UNIT	PAPER NUMBER	
			2191	<u> </u>	
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### **ACKNOWLEDGEMENT OF REQUEST**

Notice of Allowance/Allowability Mailed

The request for a copy of the initialed PTO 1449, dated 12-08-2005, has been received by the U.S. Patent and Trademark Office.

+ Requested copy attached

FOR'

571-272-4200 or 1-888-786-0101 Application Assistance Unit Office of Data Management

- EFS-Web Receipt date: 12/08/2005

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# LAPS Rec'd PCT/PTO 08 DEC 2005

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION **DISCLOSURE STATEMENT** 

SERIAL NO. ATTORNEY DOCKET NO. GB920030063US1 Not yet assigned FILING DATE GROUP ART UNIT December 8, 2005

APPLICANT(S): Stephen J. Todd

(use several sheets if necessary)

REFERENCE DESIGNATION

#### **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,305,389	04-19-1994	Palmer	382/1	08-30-1991
	A2	6,092,154	07-18-2000	Curtis et al.	711/137	11-07-1997
	A3	6,449,696	09-10-2002	Okayasu	711/137	12-23-1998
	A4	6,453,321	09-17-2002	Hill et al.	707/103	02-11-1999
	A5	2002/0002658	01-03-2002	Okayasu	711/137	12-23-1998
	A6	2003/0126116	07-03-2003	Chen et al.	707/3	12-28-2001

#### **FOREIGN PATENT DOCUMENTS**

EXAMINER	XAMINER DOCUMENT	DATE	COUNTRY	CLASS/	TRANSLATION		
INITIAL		NUMBER	DATE	COUNTRY	SUBCLASS	YES	МО
	Α7	WO 01/50275	07-12-2001	Europe	G06F 12/08		

#### **NON-PATENT DOCUMENTS**

EXAMINEF INITIAL		DOCUMENT (Including Author, Title, Source, and Pertinent Pages
	A8	Jack L. Lo et al. "An Analysis of Database Workload Performance on Simultaneous Multithreaded Processors" Computer Architecture, 1998. Proceedings, The 25 <sup>th</sup> Annual International Symposium on Barcelona, Spain 27 June – 1 July 1998, Los Alamitos, CA, USA, IEEE Comput. Soc., US, 27 June 1998, pages 39-50, XP010291385.
	A9	Yan Solihin et al. "Using a User-Level Memory Thread for Correlation Prefetching"
		Proceedings of the 29th Annual International Symposium on Computer Architecture,

EXAMINER /Matthew Brophy/ (02/28/2008)	DATE CONSIDERED	02/28/2008
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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2002, pages 171-182, XP002301172.

EXAMINER /Matthew Brophy/ (02/28/2008)

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02/28/2008

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